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| APPLICATION NO.  | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO.      | CONFIRMATION NO. |
|--|-------------|----------------------|--------------------------|------------------|
| 10/802,549   | 03/16/2004  | Eitan Cadouri        | 524322000600             | 3719             |
| 20872  | 7590        | 09/12/2005           |                          |                  |
| MORRISON & FOERSTER LLP<br>425 MARKET STREET<br>SAN FRANCISCO, CA 94105-2482 |             |                      | EXAMINER<br>DANG, PHUC T |                  |
|  |             |                      | ART UNIT                 | PAPER NUMBER     |
|  |             |                      | 2818                     |                  |
| DATE MAILED: 09/12/2005  |             |                      |                          |                  |

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

10/802,549

Applicant(s)

CADOURI, EITAN

Examiner

PHUC T. DANG

Art Unit

2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on election filed 7/28/2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) 16-20 is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-9 and 12-15 is/are rejected.
- 7) ☒ Claim(s) 10 and 11 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 16 March 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: \_\_\_\_\_

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### **DETAILED ACTION**

1. This application claims benefit of 60/454,706 filed on March 17, 2003.

#### **Election/Restrictions**

2. Claims 1-15 are considered for examining by election filed on July 28, 2005.

Claims 16-20 are non-elected claims and still pending in the application.

#### **Oath/Declaration**

3. The oath/declaration filed on March 16, 2004 is acceptable.

#### **Specification**

4. The specification has been checked to the extent necessary to determine the presence of all possible minor errors. However, the applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

#### **Claim Rejections - 35 USC § 102**

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(f) or (g) prior art under 35 U.S.C. 103(a).

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5. Claims 1-8 are rejected under 35 U.S.C. 102 (b) as being anticipated by Balamurugan (U.S. Patent No. 5,933,351).

Balamurugan discloses a method for locating dies cut from a silicon wafer comprising:

- a) obtaining a die placement of dice (dice are arranged in a pattern) to be formed on the wafer (54, Fig. 2);
- b) obtaining one or more locations (62, 66 and 68, Fig. 2) on the wafer (54, Fig. 2) contacted by one or more processing structures (Figs. 1-2) or a substance emitted by one or more processing structures (Figs. 3A-3B); and
- c) adjusting the die placement to increase yield of the dice based on the obtained one or more locations (62, 66 and 68, Fig. 2) on the wafer (54, Fig. 2).

Regarding claims 2-7, Balamurugan discloses a process for generating, determining and selecting with the highest yield from the plurality of die placement to achieve more good dice on the wafer as shown in Figs. 1-3B and col. 4, lines 36+.

Regarding claim 8, Balamurugan discloses the die placement includes an arrangement of reticle array and when each reticle array includes an arrangement of die (Figs. 2).

### **Claim Rejections - 35 USC § 103**

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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6. Claims 9, 12-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Balamurugan (U.S. Patent No. 5,933,351).

Regarding claim 9, Balamurugan does not disclose using clamps to contact one or more location in the process. However, Balamurugan discloses a wafer table (50) to hold the wafer (54) for not moving the locations in the process.

Then, it would have been obvious to one having ordinary skilled in the art at the time the invention was made to modify the teaching of Balamurugan by the above teaching for a purpose of improving a die placement in the process.

Regarding claim 12, Balamurugan teaches a method for locating dies cur from a silicon wafer comprising:

- a) obtaining a die placement of dice to be formed on the wafer (54), the die placement having an arrangement of reticle arrays (Fig. 2), wherein each reticle array (Fig. 2) includes an arrangement of dice to be formed on the wafer (54);

- b) obtaining one or more locations (62, 66 and 68) on the wafer (54); and

- c) adjusting the die placement based on the obtained one or more locations (62, 66 and 68) on the wafer (54).

Balamurugan teaches all the featues of the claimed invention as discussed above, but does not disclose using clamps to contact the wafer in the process. However, Balamurugan discloses a wafer table (50) to hold the wafer (54) for not moving the locations in the process.

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Then, it would have been obvious to one having ordinary skilled in the art at the time the invention was made to modify the teaching of Balamurugan by the above teaching for a purpose of improving a die placement in the process.

Regarding claims 13 and 15, Balamurugan discloses a process for generating, determining and selecting with the highest yield from the plurality of die placement to achieve more good dice on the wafer as shown in Figs. 1-3B and col. 4, lines 36+.

Regarding claim 14, Balamurugan discloses a further step comprising adjusting the one or more locations on the wafer based on the die placement, wherein the die placement and the one or more locations on the wafer are adjusted together (Figs. 2 and 3A-3B).

#### **Allowable Subject Matter**

7. The following is a statement of reason for the indication of allowable subject matter:

Claims 10-11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

None of the prior art of records discloses the one or more processing structures includes a jet that emit a chemical solution or water at the wafer as cited in claim 10 and the die are formed on a first surface on the wafer, and wherein the one or more locations are on the first surface of the wafer or on a second surface on the wafer opposite the first surface as cited in claim 11.

#### **Conclusion**

8. Applicants are advised to cancel the non-elected claims 16-20 upon response to the next Office action if the application is considered to be allowed.

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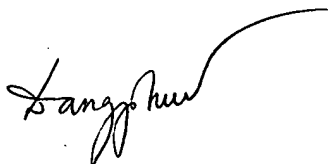
9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuc T. Dang whose telephone number is 571-272-1776. The examiner can normally be reached on 8:00 am-5:00 pm.

10. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Nelms can be reached on (571) 272-1787. The fax phone numbers for the organization where this application or proceeding is assigned are 703-872-9306 for regular communications and Final communications.

11. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Phuc T. Dang

PV

A handwritten signature in black ink, appearing to read 'Phuc T. Dang', with a long, sweeping horizontal line extending to the right.

Examiner

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